

Electronic Acknowledgement Receipt

EFS ID:	1970396
Application Number:	10720585
International Application Number:	
Confirmation Number:	5059
Title of Invention:	Method and apparatus to extend the number of instruction bits in processors with fixed length instructions, in a manner compatible with existing code
First Named Inventor/Applicant Name:	Erik R. Altman
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Filer:	Mark F. Harrington/Ann Okrentowich
Filer Authorized By:	Mark F. Harrington
Attorney Docket Number:	YOR920030405US1
Receipt Date:	13-JUL-2007
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part .zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	Response7-13-07.pdf	390786 3a51a460fd924d9012c1d210e871621 9871dd	no	10

Warnings:

Information:	Total Files Size (in bytes):	390786
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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.		
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